

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

EL 964691864 US

1/13

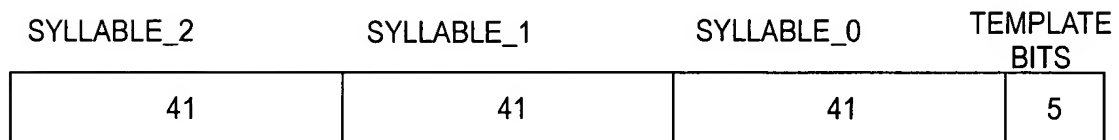



FIG. 1

100



400

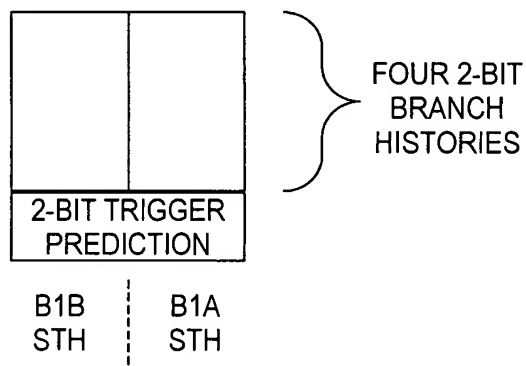
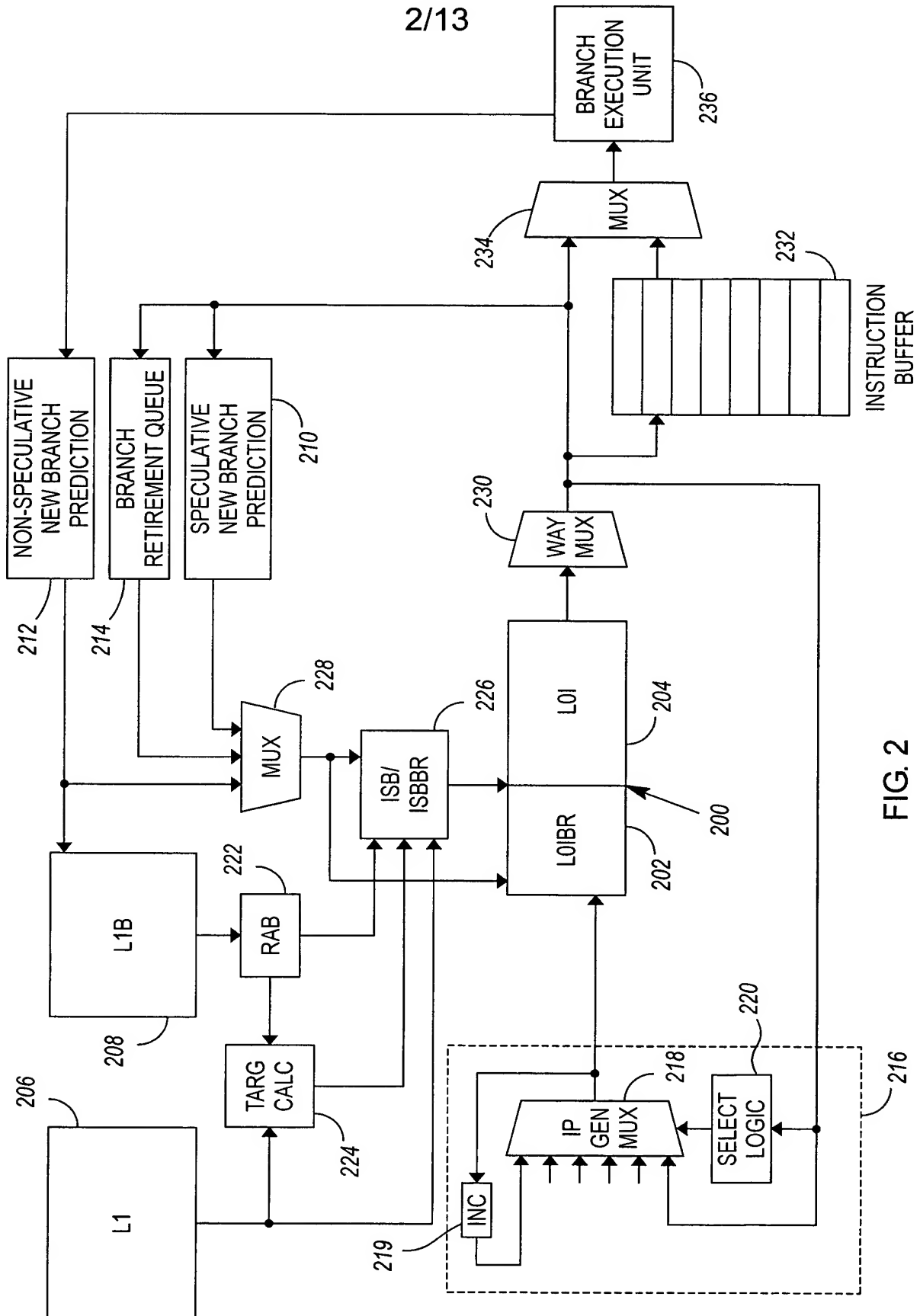


FIG. 4

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2



"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

3/13

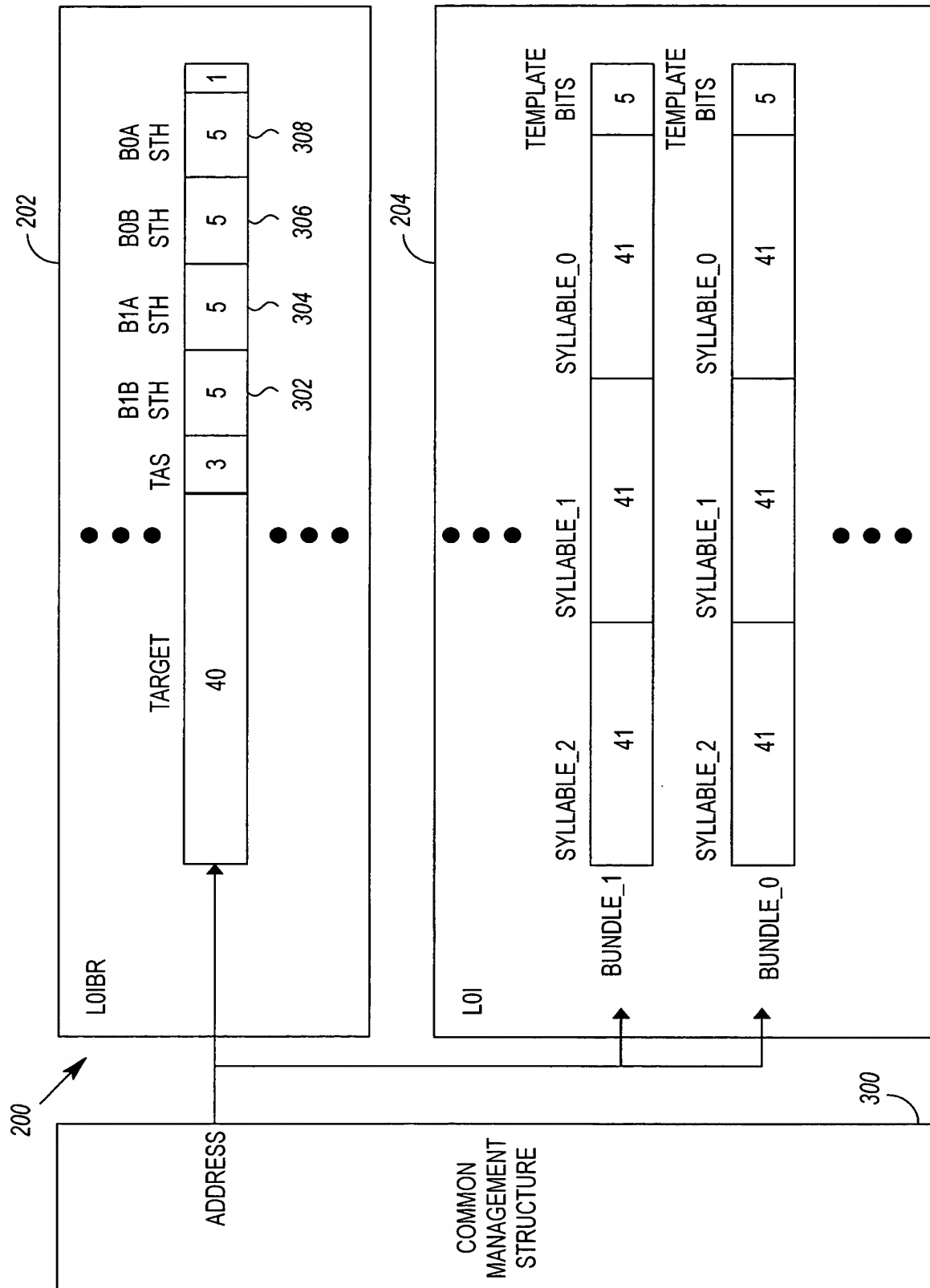


FIG. 3

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

4/13

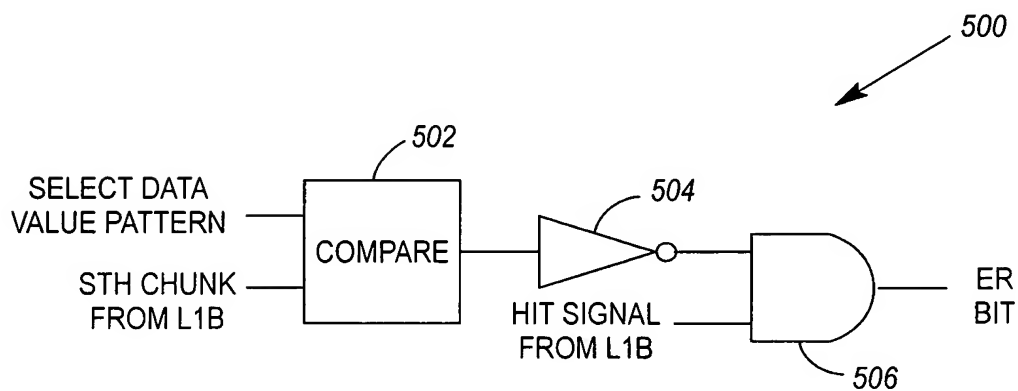


FIG. 5

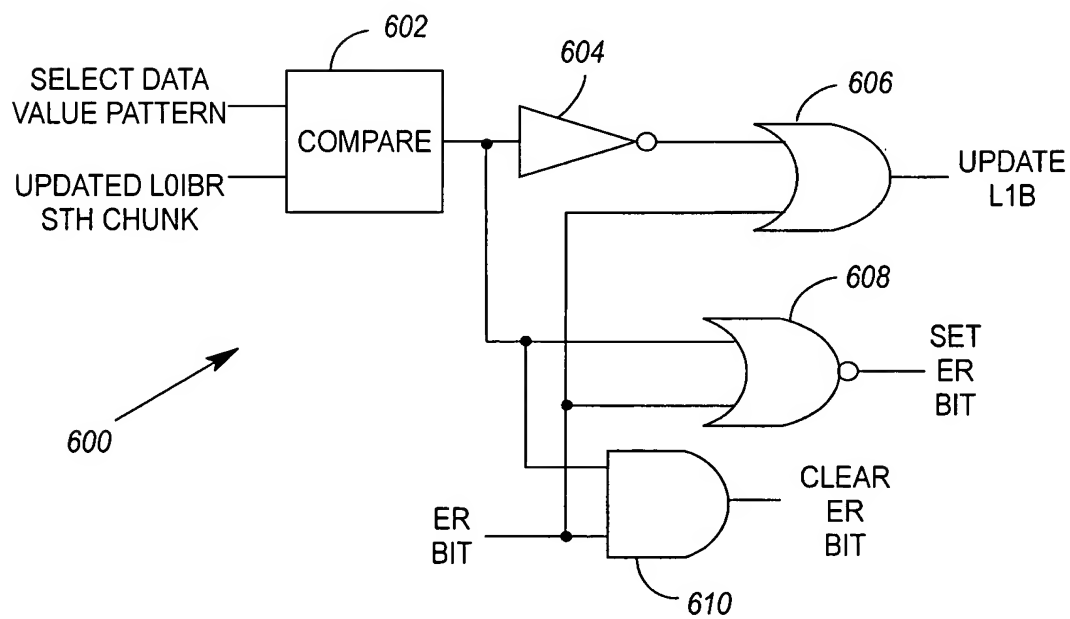


FIG. 6

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

5/13

ARCHITECTURE	INSTRUCTION	RANGE
PA RISC 2.0	B	PC $\pm$ 256 KB
	BLL	PC $\pm$ 8 MB
Sparc v9	BA.a	PC $\pm$ 8 MB
	Jump	reg $\pm$ 16 KB
PowerPC	B	PC $\pm$ 32 MB
MIPS IV	J	256 MB aligned region
	B<cond>	PC $\pm$ 128 KB
Alpha	Branch	PC $\pm$ 4 MB
	Jmp	register $\pm$ 16 KB
IA-64 v2.4	BR target	PC $\pm$ 16 MB

FIG. 7

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.  
Docket No. - 10982165-2

6/13

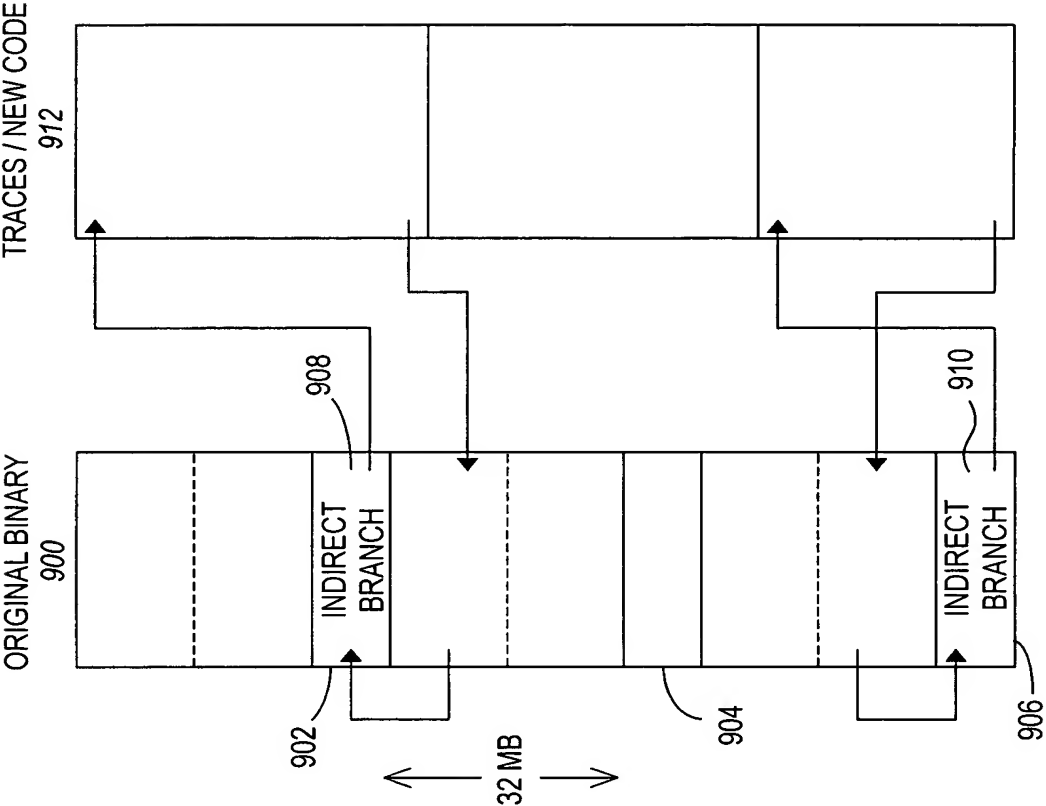


FIG. 9

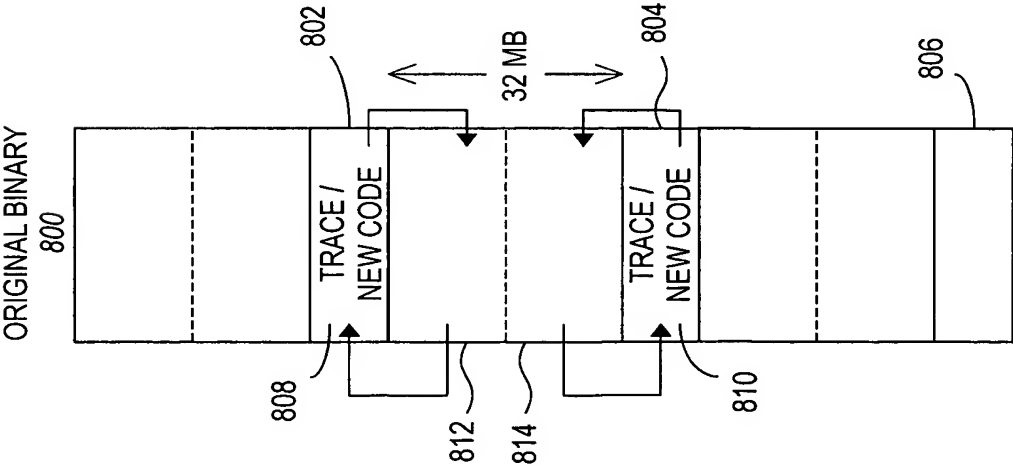


FIG. 8

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

7/13

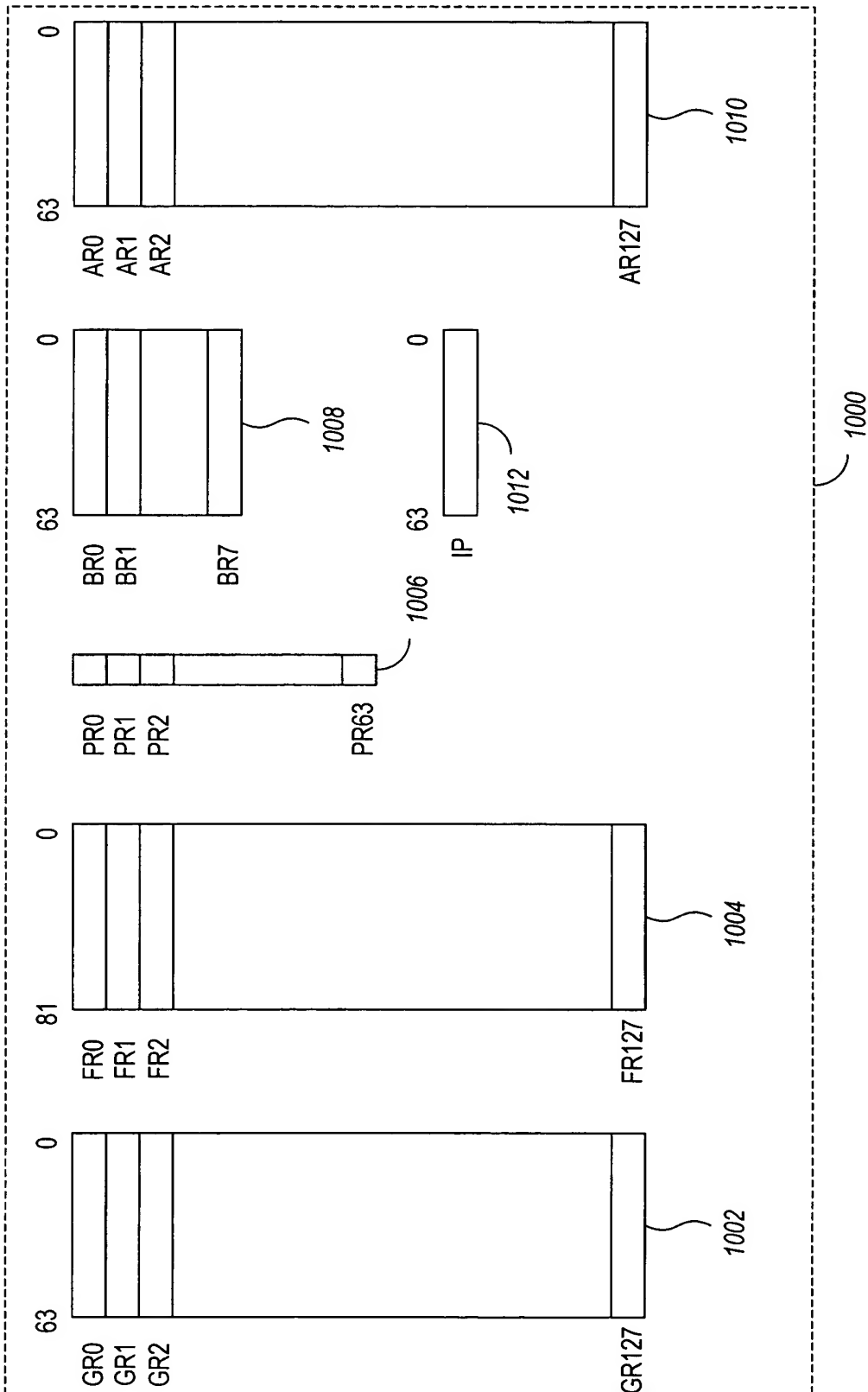


FIG. 10

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

8/13

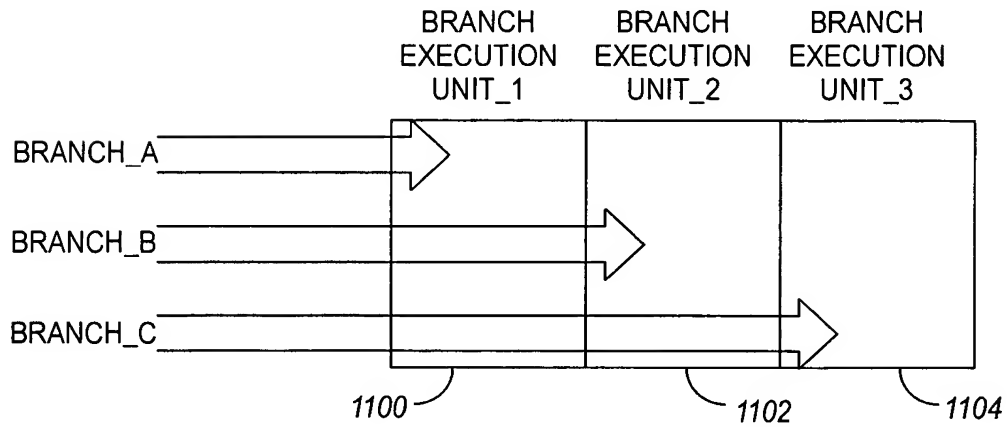


FIG. 11

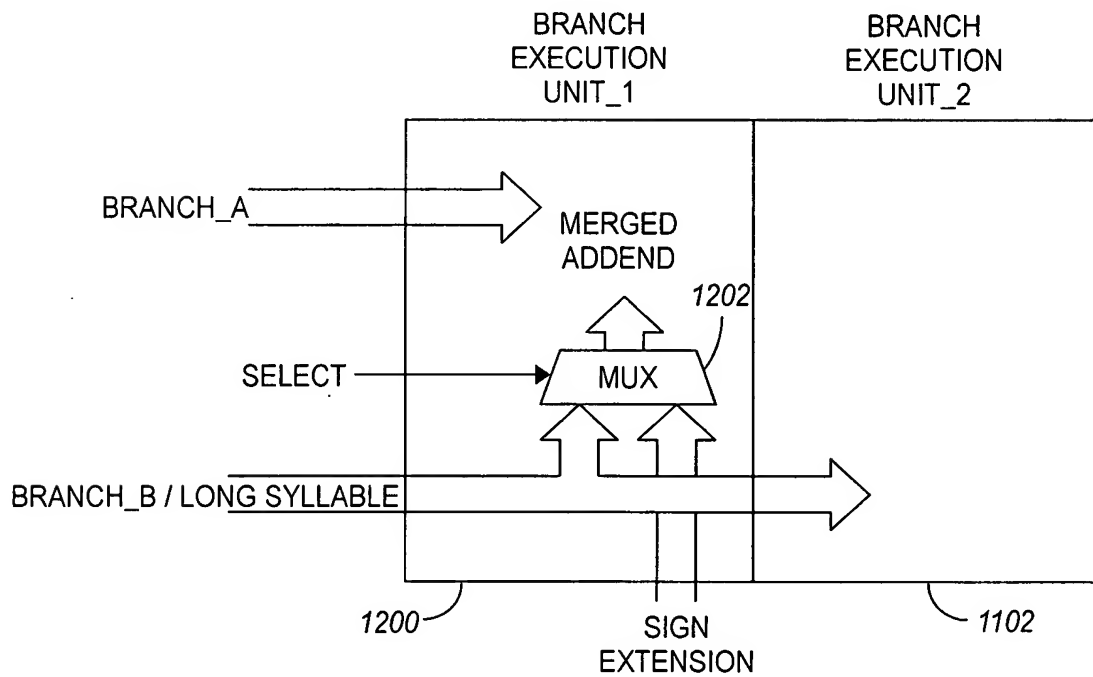


FIG. 12



"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.  
Docket No. - 10982165-2

9/13

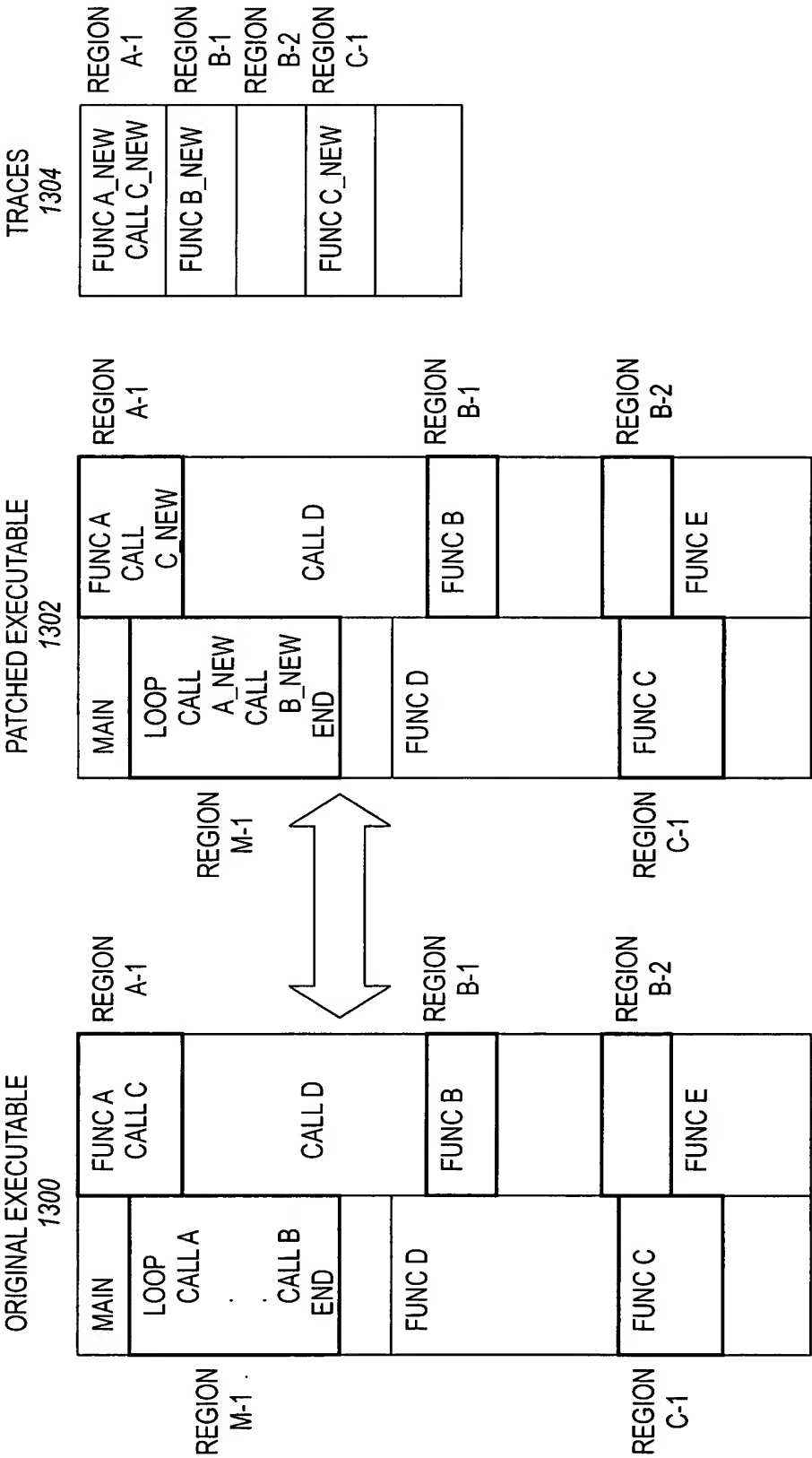


FIG. 13

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

10/13

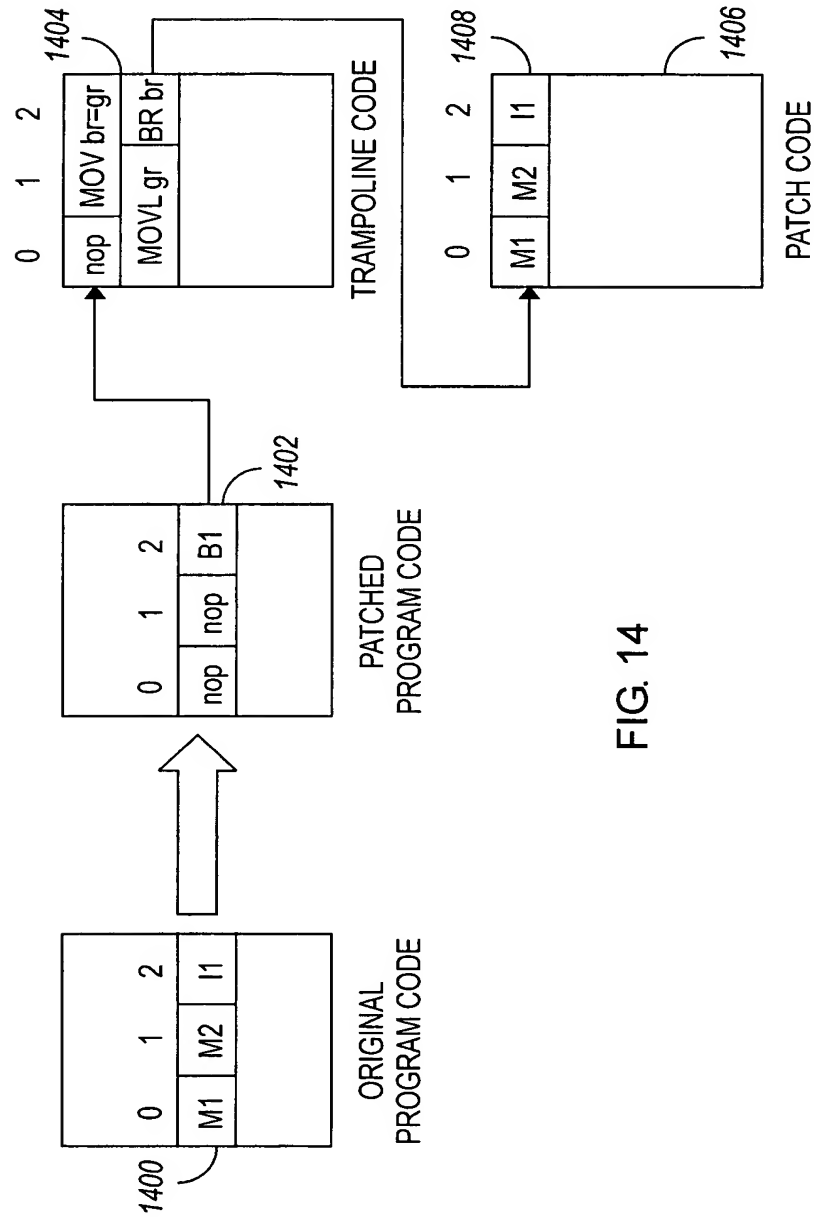


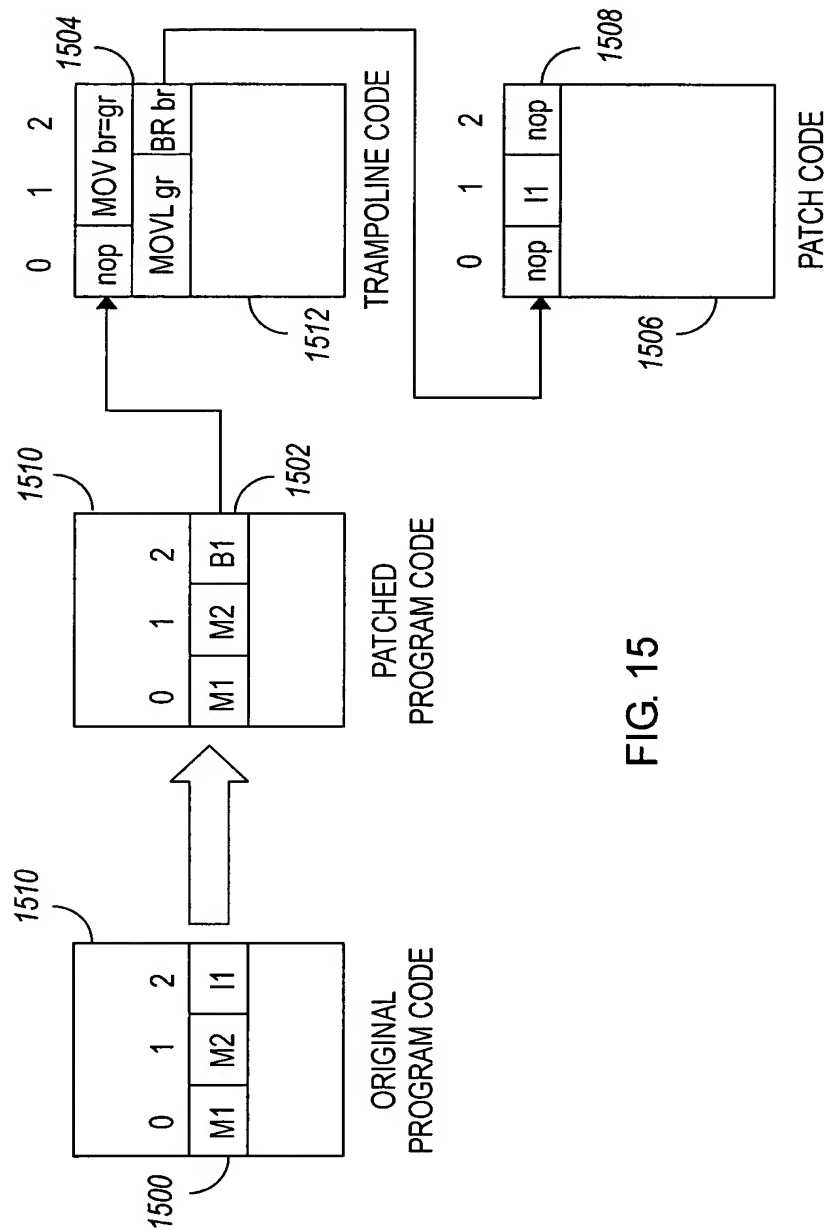
FIG. 14

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

11/13



"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

12/13

ORIGINAL BUNDLE	PATCHED BUNDLE	TARGET BUNDLE(S)
MII (m1-i1-i2)	MIB (m1-i1-B)	MII (nop-i1-i2)
MI,I (m1-i1, i2)	MIB (m1-i1-B)	MI,I (nop-i1, i2)
MLI (m1-LI)	MIB (m1-nop-B)	MLI (nop-LI)
MMI (m1-m2-i1)	MMB (m1-m2-B)	MMI (nop-m2-i1)
M,MI (m1, m2-i1)	MBB (m1-B1-B2)	MII (m2-nop-nop) MII (nop-i1-nop)
MFI (m1-f1-i1)	MFB (m1-f1-B)	MIB (nop-i1-nop)
MMF (m1-m2-f1)	MMB (m1-m2-B)	MFI (nop-f1-nop)
MIB (m1-i1-b1)	MIB (m1-i1-B)	MIB (nop-nop-b1)
MBB (m1-b1-b2)	MBB (m1-b1-B)	MIB (nop-nop-b2)
BBB (b1-b2-b3)	BBB (b1-b2-B)	MIB (nop-nop-b3)
MMB (m1-m2-b1)	MMB (m1-m2-B)	MIB (nop-nop-b1)
MFB (m1-f1-b1)	MFB (m1-f1-B)	MIB (nop-nop-b1)

FIG. 16

"ROUTING OF WIRES CARRYING SINGLE-SYLLABLE IP-RELATIVE BRANCH INSTRUCTIONS  
AND LONG IP-RELATIVE BRANCH INSTRUCTIONS"

Inventor - James E. McCormick, Jr., et al.

Docket No. - 10982165-2

13/13

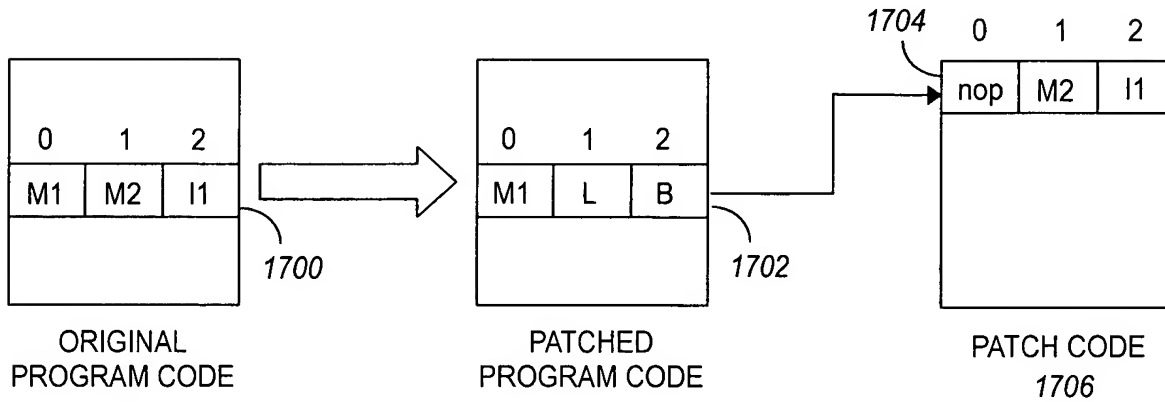


FIG. 17

ORIGINAL BUNDLE	PATCHED BUNDLE	TARGET BUNDLE
MII (m1-il-i2)	MLB (m1-LB)	MII (nop-il-i2)
MI,I (m1-il, i2)	MLB (m1-LB)	MI,I (nop-il, i2)
MLI (m1-LI)	MLB (m1-LB)	MLI (nop-LI)
MMI (m1-m2-il)	MLB (m1-LB)	MMI (nop-m2-il)
M,MI (m1, m2-il)	MLB (m1-LB)	MMI (nop-m2-il)
MFI (m1-fl-il)	MLB (m1-LB)	MFI (nop-fl-il)
MMF (m1-m2-fl)	MLB (m1-LB)	MMF (nop-m2-fl)
MIB (m1-il-b1)	MLB (m1-LB)	MIB (nop-il-b1)
MBB (m1-b1-b2)	MLB (m1-LB)	MBB (nop-b1-b2)
BBB (b1-b2-b3)	MLB (m1-LB)	BBB (b1-b2-b3)
MMB (m1-m2-b1)	MLB (m1-LB)	MMB (nop-m2-b1)
MFB (m1-fl-b1)	MLB (m1-LB)	MFB (nop-fl-b1)

FIG. 18